



RAFAEL SILVA

ELECTRICAL ENGINEERING

Rafael Silva, 23 (1998),
Portuguese, Natural from Leiria.

Self Learner and always willing to.

Interested in, Open-source, Electric
Vehicles, Robotics, Automation, and Input
devices.

Integrated into Electronics and Robotics
Clubs.

Project development.

Practiced federated sports for 10 years
(Handball) 2006 - 2016.

EDUCATION

ESTG Politécnico de Leiria (2017 - 2019)

TeSP in Electronics and
Telecommunication Networks [17/20]

ETAP Pombal (2014 - 2017)

Mechatronics Technician [16/20]

CONTACTS

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Leiria, Portugal

linkedin.com/in/rafael-silva-aka-perigoso

github.com/perigoso

gitlab.com/perigoso

perigoso.github.io

SKILLS

- Design PCBs (Altium, Eagle, KiCad)
- Design Analog & Digital circuits
- Linux
- C, *Embedded C*
- Python
- Reverse Engineering
- CAD (FreeCad, Fusion 360)
- Industrial maintenance / repair
- Git
- SQL
- *Spreadsheets*
- Portuguese (First Language)
- Proficient English (C2 Level)

EXPERIENCE

Firmware Developer (Full-Time)

AJTEC - sistemas de informação
2021 - Present (as of 2022)

Repair Technician (Part-Time), Hardware and Firmware Design (Full Stack Embedded) (Freelance)

Globalsilva, Lda.
2014 - 2021

R&D Research Technician (Scientific initiation scholarship)

Universidade de Coimbra
nov. 2019 - jun. 2020

R&D Lab Technician (Internship)

INESC Coimbra.
feb. 2019 - jun. 2019

Industrial Electrical Board Department (Internship)

EST SGPS, S.A.
may. 2015 - jul. 2015

CNC Maintenance Technician (Internship)

IMPL Mecânica de Precisão, Lda.
jan. 2014 - feb. 2014

PROJECTS

OpenEEW

Hardware Design Lead (2020 - Present)
openeew.com